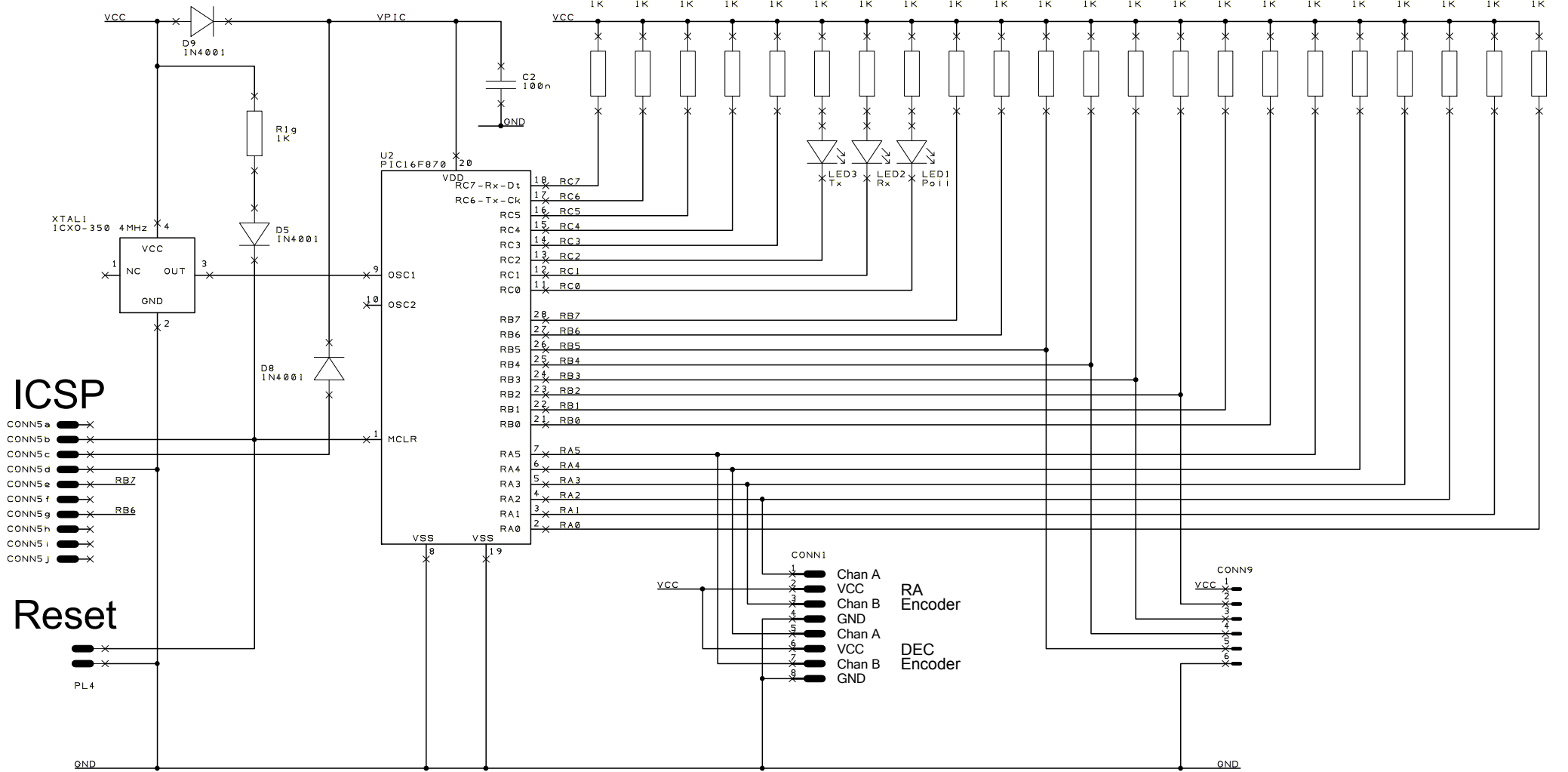
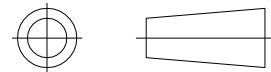


CPU



Encoder Inputs



DSC Project MK II
CPU And ICSP Interface
Encoder Inputs And Port B